IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/599,181 Confirmation No. 5552

Applicant : Aramatsu

Filed : 09/21/2006

TC/A.U.

Examiner :

Docket No. : TW08-P06178US

Customer No. : 33356

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Information Disclosure Statement

Dear Sir:

General Matters

X Copies of the references listed on the attached form PTO/SB/08A are enclosed. Copies of US patents and patent applications are not enclosed.

This Information Disclosure Statement is being filed under:

X 37 CFR 1.97(b)(3) Before the mailing of a first Office action on the merits.	
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X Concise Statement Under 37 CFR 1.98(3)(i): X The following is a concise explanation of the relevance, as it is presently understood by the individual most knowledgeable about the content of the information, of each patent, publication, or other information listed that is not in the English language:

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH, /J.H./

JP 3-119889 -- Continuous pictures picked up from two directions or multiple directions are displayed as a longitudinal slit picture on a picture display screen of the picture display device. For example, by using a transmissive liquid crystal display element, etc., X and Y addresses are designated by a control means such as a microcomputer, etc., and the barrier striped in the arbitrary shape are formed at an arbitrary positions on a barrier surface. It is in the case of three-dimensional picture display to generate the longitudinal stripe-shape barrier stripes on a barrier. In the case of two-dimensional picture display, the above mentioned barrier stripes are stopped being generated and the drive of the above mentioned barrier is controlled so as to obtain an uncolored and transparent state over the whole area of a picture display area. Thus, the device can be also used as the two-dimensional picture display device.

JP 10-142572 — A spatial light modulator has a modulation area capable of operating in two modes. In a first clear mode, the area is continuously and substantially uniformly transparent. In a 2nd or a barrier mode, the area is changed over and parallax barrier having a slit separated by a continuous opaque area is formed. Such a modulator is used with an image liquid crystal display, and provides a 3D mode capable of pursuing a movement of an observer and an automatic stereoscopic 3D display having a 2d mode using a maximum resolution of LCD therein.

JP 2001-1166259 -- A display with a light shield means is divided into three areas. A light shield part of the light shield means can be moved, area by area, by ¼ of the light shield part pitch. In the ¼ movement, an image passes corresponding to individual ranges after 'shifting'. A video display surface is also divided into areas corresponding to the mentioned area divisions and the display order of striped left-and right-eye images is controlled by the areas. The ¼ movement is not performed in an area H2 but in areas H1 and H3. Further, only in the area H1, the left- and right-eye images are switched. In this case, the right-eye image passes through L1° from the area H1 and enters the right eye of the observer, the right eye-image oases through R2 from the area H2 and enters the right eye, and the right-eye image passes through R2° from the area H3 and enters the right eye. Thus, only the right eye image can be supplied to the right eye of the observer having moved back from optimum observation position D.

Deposit Account Authorization

Please apply any charges or credits to Deposit Account No. 503456.

Respectfully submitted,

prince Santite

Date: November 17, 2006

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /J.H./

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Substitute for form 1449/PTO

Complete if Known

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1

Complete if Known			
Application Number	10/599,181		
Filing Date	09/21/2006		
First Named Inventor	Aramatsu		
Art Unit			
Examiner Name			
Attorney Docket Number	TW08-P06178US		

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No 1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	
		Country Code ³ Number ⁴ King Code ⁵ (if known)	MM-DD-YYYY		Or Relevant Figures Appear	T⁵
		JP 3-119889	05-22-1991	Isono Haruo		
		JP 10-142572	05-29-1998	Harrold Jonathan		
		JP 2001-1166259	06-22-2001	Masutani Takeshi		

Examiner Signature	/James Hughes/	Date Considered	12/20/2008

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional). See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. Here office that issued the document, by the two-letter code (WIPO Standard ST.3). For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the senal number of the patent document. Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patenl and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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